

Appln. No.: 10/713,450

Amdt. dated April 19, 2006

**Amendments to the Specification:**

Please replace the paragraph at page 25, lines 30-34 (paragraph 92 in the published application) with the following paragraph:

FIG. 13 is a block diagram illustrating a modification of the receiver of FIG. 4 to receive a trellis coded signal. In FIG. 13 the 32-inverse Fourier Transform provides symbols (S) along with the complex conjugate of the signals ( $S^*$ ) to a trellis decoder. The trellis decoder decodes the symbols and then provides a 4 bit output signal to the XGMII to combine with other signals in order to produce the output signal.